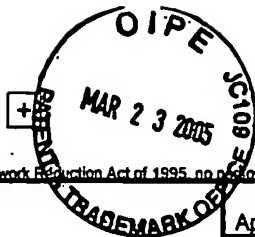


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PTO/SB/08a (08-03)

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of

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Application Number

10/756,122

Filing Date

JANUARY 13, 2004

First Named Inventor

YAU, ET AL.

Group Art Unit

2823

Examiner Name

JULIO J. MALDONADO

Attorney Docket Number

AMAT/2592.C7/DSM/LOW K/JW

Submission Date

3/21, 2005

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
<i>[Signature]</i>	A1	US-5,000,113	03/19/1991	WANG, ET AL.	Figs. 1-19
	A2				
	A3				
	A4				
	A5				
	A6				
	A7				
	A8				
	A9				
	A10				
	A11				
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	A13				
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	A16				
	A17				
	A18				

FOREIGN PATENT DOCUMENTS

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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
<i>[Signature]</i>	B1	EP 1 123 991 A2	08/16/2001	MICHAEL A. TODD	[0007] - [0015]	
	B2	EP 0 960 958 A2	12/01/1999	LOBODA, ET AL.	[0003] - [0014]	
	B3	WO 94/01885 A1	01/20/1994	DOBSON, ET AL.	pg. 2 ln. 2 - pg. 7 ln. 37	
	B4					
	B5					

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[Signature: Julio J. Maldonado]

Date Considered

03/13/2005

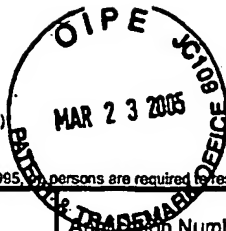
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Application Number

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Filing Date

JANUARY 13, 2004

First Named Inventor

YAU, ET AL.

Group Art Unit

2823

Examiner Name

JULIO J. MALDONADO

Attorney Docket Number

AMAT/2592.C7/DSM/LOW K/JW

Submission Date

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
je	C1	BAKLI, ET AL., "Materials and Processing for .025 μ m Multilevel Interconnect," Microelectronic Engineering 33 (1997) pp. 175-188.	
je	C2	BHAN, ET AL., "Deposition of Stable Low K and High Deposition Rate SiF ₄ -Doped TEOS Fluorinated Silicon Dioxide (SiOF) Films," Thin Solid Films 308-309 (1997) pp. 507-511.	
je	C3	C.F. LIN, ET AL., "Improved Ozone-Tetraethoxysilane Oxide Reliability for Deep Submicron Inter-Metal Dielectric Applications by Deposition of a Silane-Based Oxide Underlayer," Thin Solid Films 308-309 (1997) pp. 621-626.	
je	C4	European Search Report from European Patent Office for 99906897.6 (2592.EP), dated 01/10/2005.	
	C5		
	C6		
	C7		
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	C10		
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